

**SOLID MODELING: ARCHITECTURES,
MATHEMATICS, AND ALGORITHMS**

COURSE # 20

CHAIR:

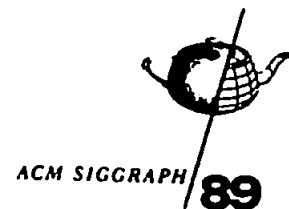
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SPEAKERS:

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Solid Modeling: Architectures, Mathematics, and Algorithms

Course Abstract

In this course we will study issues involved in the design and construction of contemporary solid modeling systems. It is impossible in one day to provide sufficient material so that one could go out and design such a system from scratch, so our goal is to present a framework in which to understand the basic design and architectural considerations. We begin with a brief review of pertinent background concepts and an overview of the assumed overall modeling environment. We then present relevant high-level architectural considerations with an emphasis on dual representation (CSG-BRep) solid modeling systems. The importance of a boundary evaluation algorithm in such environments will be introduced, and an approach to such an algorithm described. This will lead to the need for robust curve and surface representations and intersection algorithms, descriptions of which will follow. The importance of efficient and comprehensive representation schemes for Boundary Representations will also be apparent, so contemporary approaches will be presented next. Finally we examine the important topic of Features in modeling systems. Applications of features, feature recognition, and designing with features will be considered.

Speaker Biographies

George Allen is a staff scientist with McDonnell Douglas Manufacturing and Engineering Systems Company, where he has been involved in the development of commercial CAD/CAM systems since 1977. He also teaches a graduate course in computational geometry at the University of California at Irvine. Allen holds B.Sc. and M.A. degrees in mathematics and has authored a variety of talks and articles on geometric modeling and other aspects of CAD/CAM. He has been teaching SIGGRAPH courses since 1982.

James R. Miller is an Associate Professor of Computer Science at The University of Kansas in Lawrence where he is involved in teaching and research in computer graphics and geometric modeling. He has been involved in research and prototype development activities in the general area of graphics for design since 1976, and specifically in solid modeling for mechanical CAD/CAM since 1980. Prior to joining the faculty at the University of Kansas, Dr. Miller spent eight years with Control Data Corporation where his duties included research, development, and planning for various commercial CAD/CAM products. He is the author of several papers and talks on solid modeling, and has been involved in the SIGGRAPH solid modeling courses since 1984.

Kevin J. Weiler is Director of Advanced Graphics at Ardent Computer. His fields of interest are computer graphics, where his best known work has been in hidden surface removal, polygon clipping, and algorithms; and geometric modeling, where he has worked in topological representations, topological sufficiency, nonmanifold geometric modeling representations, and algorithms. Prior to joining Ardent Computer, Dr. Weiler was a graphics scientist at General Electric Corporate R&D, and previously he was Senior Scientist and Director of the CAD-Graphics Laboratory at Carnegie-Mellon University. He has been an adjunct faculty member at Carnegie-Mellon and Rensselaer Universities. Dr. Weiler earned a B.Arch. from the University of Maryland in 1975, an M.S. from Cornell University in 1977, and a Ph.D. from Rensselaer University in 1986.

Peter R. Wilson is Visiting Research Professor in the Electrical and Computer Systems Engineering Department at Rensselaer Polytechnic Institute on leave of absence from the GE Corporate Research & Development Center. His research interests have been primarily in the field of solid modeling with an emphasis on feature based systems and applications of solid modeling. More recently this has led to an interest in information modeling technology and applications. In an earlier incarnation, Dr. Wilson was a semiconductor device physicist. He has had many papers published on the mathematics of semiconductors and CAD systems. He is an active participant in the development of the ISO Standard for the Exchange of Product Model Data (PDES/STEP).

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Table of Contents

Section I:	Architectural Issues in Solid Modelers (slides)	Miller	I-1 to I-22
Section II:	Architectural Issues in Solid Modelers (paper)	Miller	II-1 to II-26
Section III:	A Boundary Evaluation Algorithm	Miller	III-1 to III-19
Section IV:	Geometric Representations & Computations	Allen	IV-1 to IV-56
Section V:	Geometric Approaches to Nonplanar Quadric Surface Intersection Curves	Miller	V-1 to V-34
Section VI:	Edge-Based Data Structures for Solid Modeling in Curved-Surface Environments	Weiler	VI-1 to VI-20
Section VII:	Two Taxonomies for Geometric Modeling Representations	Weiler	VII-1 to VII-9
Section VIII:	Non-Manifold Geometric Boundary Modeling	Weiler	VIII-1 to VIII-45
Section IX:	Feature Modeling Overview	Wilson	IX-1 to IX-47
Section X:	A Taxonomy of Features For Solid Modeling	Wilson	X-1 to X-11